

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- Surface-mounted package
- Low gate charge

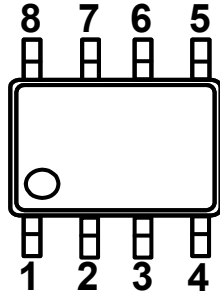
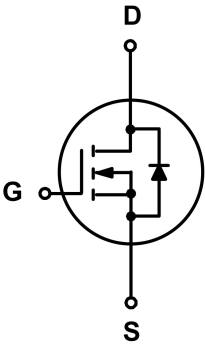
1.2 Applications

- Motor driver appliances
- Adapter appliances
- High power inverter system

1.3 Quick reference

- $BV \geq 80\text{ V}$
- $P_{tot} \cong 2\text{ W}$
- $I_D \cong 15\text{ A}$
- $R_{DS(ON)} \leq 8\text{ m}\Omega @ V_{GS} = 10\text{ V}$
- $R_{DS(ON)} \leq 16\text{ m}\Omega @ V_{GS} = 4.5\text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1,2,3	Source	 <p>Top View SOP-8L</p>	
4	Gate		
5,6,7,8	Drain		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	Drain-Source Voltage	T _A = 25 °C	80	-	V
V _{GS}	Gate-Source Voltage	T _A = 25 °C	-	± 20	V
I _D *	Drain Current	T _A = 25 °C, V _{GS} = 10 V	-	15	A
I _{DM} *	Pulsed Drain Current	T _A = 25 °C, V _{GS} = 10 V	-	50	A
P _{tot} *	Total Power Dissipation	T _A = 25 °C	-	2	W
T _{stg}	Storage Temperature		- 55	150	°C
T _J	Junction Temperature		-	150	°C
R _{θJC} *	Thermal Resistance- Junction to Case		-	3.5	°C / W
R _{θJA} *	Thermal Resistance- Junction to Ambient		-	62.5	°C / W

Notes :

- * Surface Mounted on 1 in² pad area, t ≤ 10 sec
- ** Pulse width ≤ 10 μs, duty cycle ≤ 1 %
- *** limited by bonding wire

4. Marking Information

Product Name	Marking
KJ8010S	<div style="display: flex; align-items: center;"> <div style="background-color: black; color: white; padding: 5px; margin-right: 10px;"> 8010 YWWXXX </div> <div> YWWXXX: Date Code </div> </div>

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ8010S	SOP8			3000	

Note: KUAIJIEXIN defines “ Green ” as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)

6. Electrical Characteristics (T_A = 25 °C Unless Otherwise Noted)

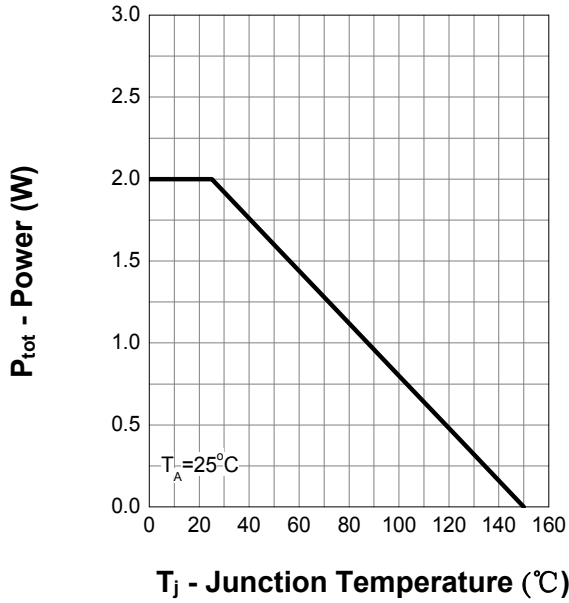
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
B _V DSS	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _{DS} = 250 μA	80	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _{DS} = 250 μA	1.0	-	3.0	V
I _{DSS}	Drain Leakage Current	V _{DS} = 64 V, V _{GS} = 0 V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{GS} = ± 20 V, V _{DS} = 0 V	-	-	± 100	nA
R _{DS(ON)} ^a	Channel On-State Resistance	V _{GS} = 10 V, I _D = 10 A	-	7	8	mΩ
	Channel On-State Resistance	V _{GS} = 4.5 V, I _D = 5 A	-	13	16	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} = 10 A, V _{GS} = 0 V	-	-	1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} = 10 A, dI _{SD} / dt = 100 A / μs	-	46	-	nS
Q _{rr}	Reverse Recovery Charge		-	47	-	nC
Dynamic Characteristics^b						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = 40 V Frequency = 1 MHz	-	1866	-	pF
C _{oss}	Output Capacitance		-	333	-	
C _{rss}	Reverse Transfer Capacitance		-	24	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} = 40 V, V _{GEN} = 10 V, R _G = 4.5 Ω, R _L = 4 Ω, I _{DS} = 10 A	-	9.2	-	nS
t _r	Turn-on Rise Time		-	21	-	
t _{d(off)}	Turn-off Delay Time		-	29	-	
t _f	Turn-off Fall Time		-	20	-	
Gate Charge Characteristics^b						
Q _g	Total Gate Charge	V _{GS} = 10 V, V _{DS} = 40 V, I _{DS} = 10 A	-	38	-	nC
Q _{gs}	Gate-Source Charge		-	8.8	-	
Q _{gd}	Gate-Drain Charge		-	9.4	-	

Notes :

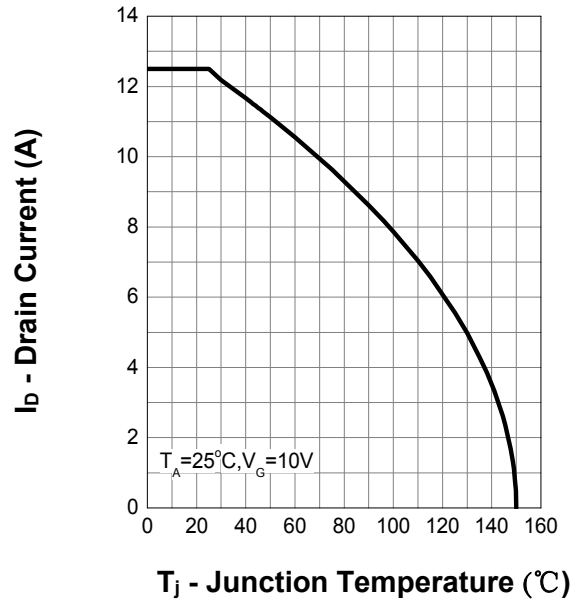
- a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2 %
- b : Guaranteed by design, not subject to production testing

7. Typical Characteristics

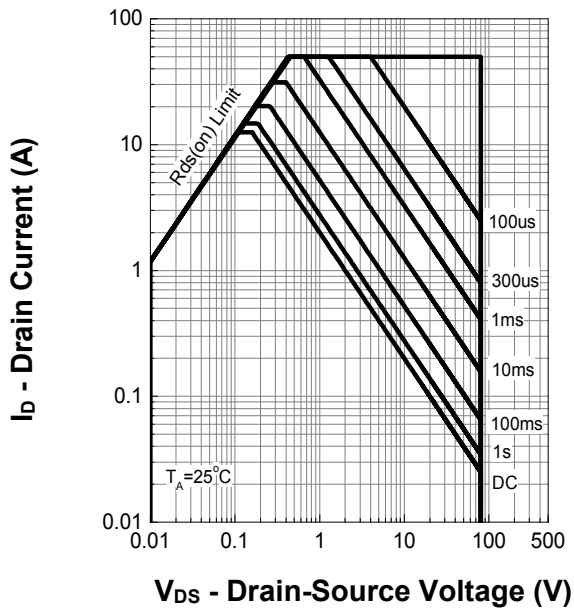
Power Dissipation



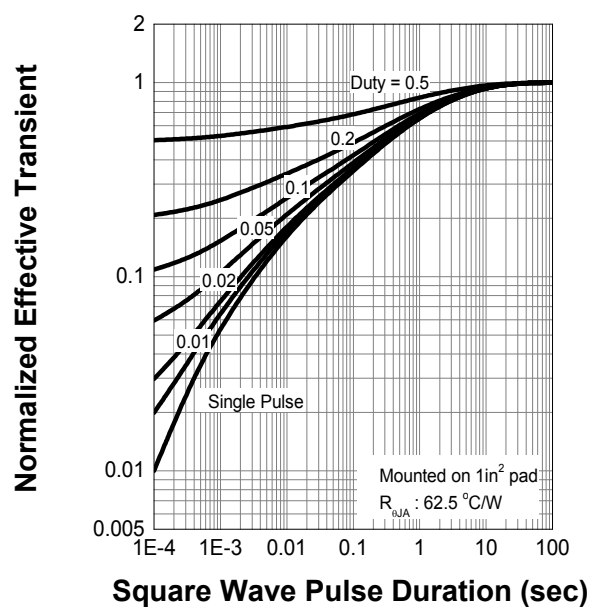
Drain Current



Safe Operation Area

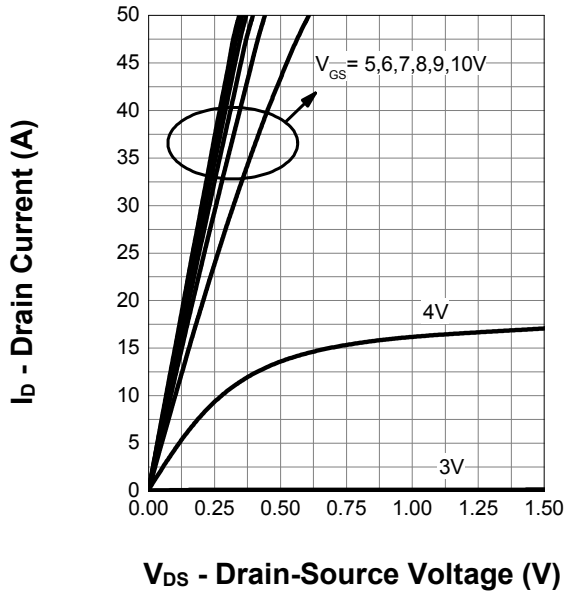


Thermal Transient Impedance

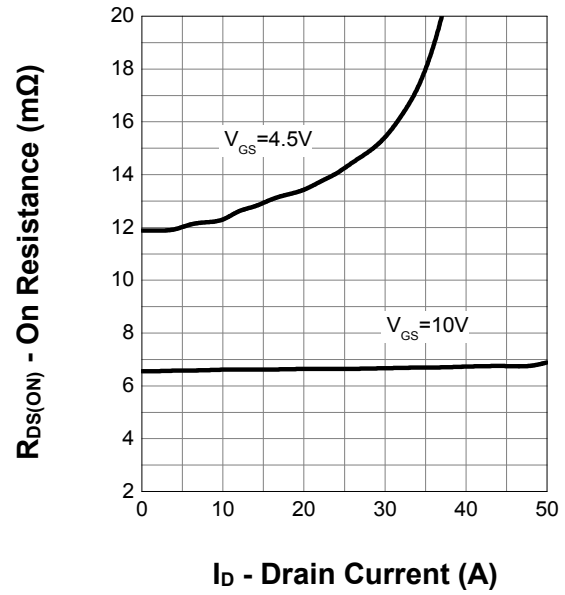


7. Typical Characteristics (cont.)

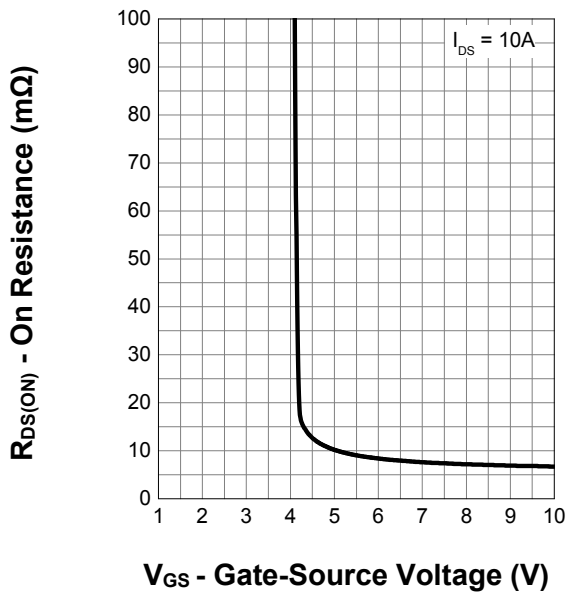
Output Characteristics



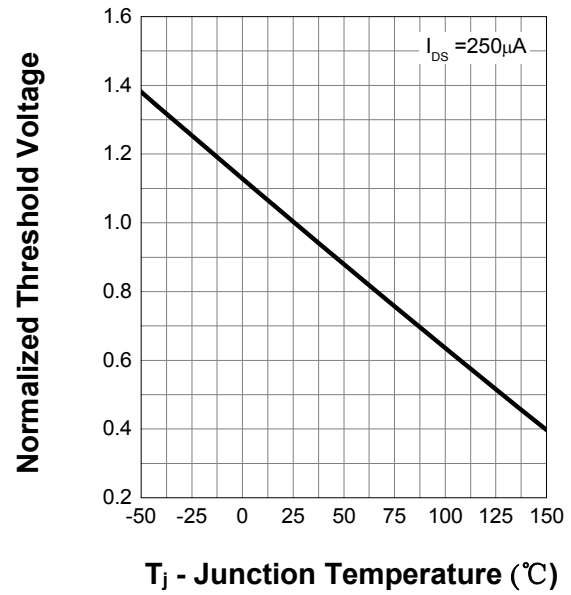
Drain-Source On Resistance



Transfer Characteristics

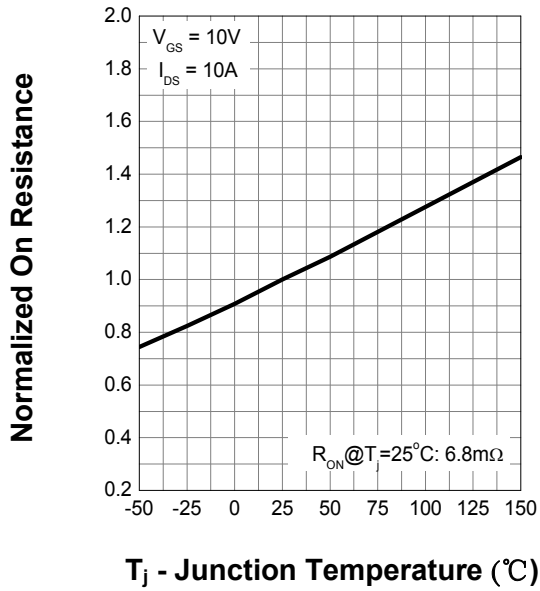


Gate Threshold Voltage

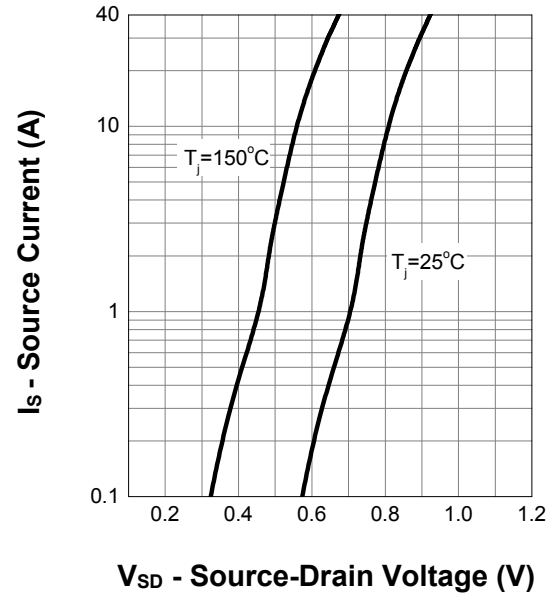


7. Typical Characteristics (cont.)

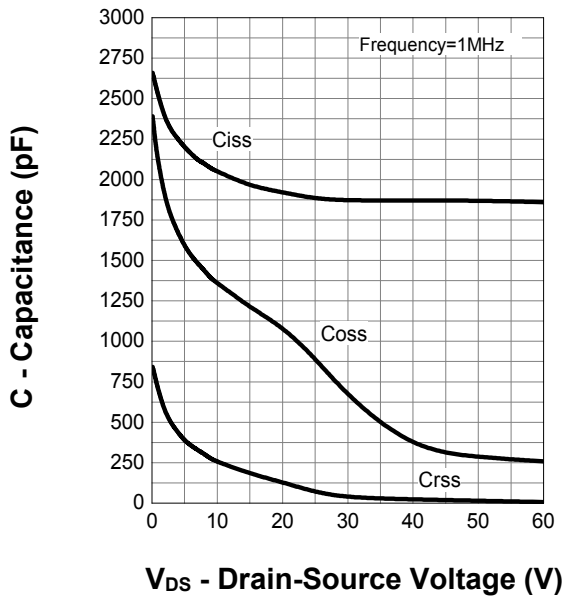
Drain-Source On Resistance



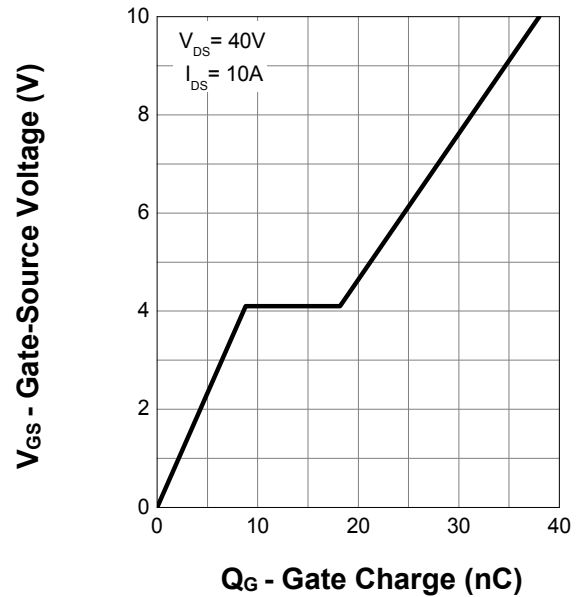
Source-Drain Diode Forward



Capacitance

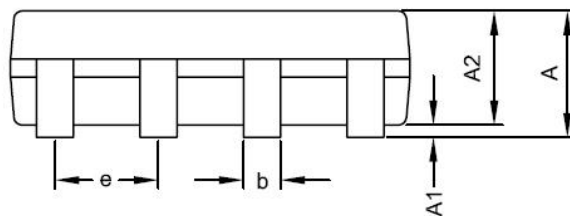
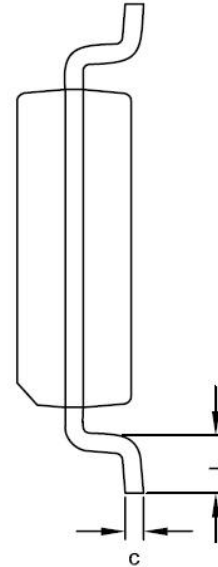
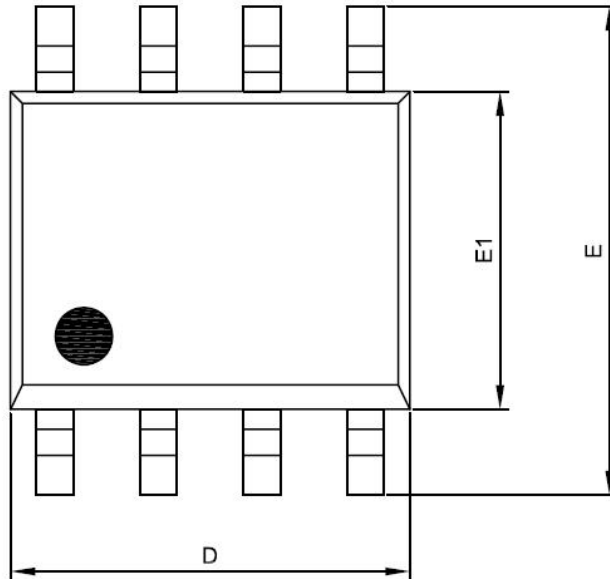


Gate Charge



8. Package Dimensions

SOP- 8L



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	1.35	1.75
A1	0.00	0.25
A2	1.15	1.50
D	4.80	5.00
E	5.80	6.20
E1	3.80	4.00
c	0.19	0.27
b	0.33	0.53
e	1.27 BSC	
L	0.40	1.27

Notes :

1. Jedec outline : MS-012AA
2. Dimensions " D " does not include mold flash, protrusions and gate burrs shall not exceed .15 mm (.006 in) per side .
3. Dimensions " E1 " does not include inter-lead flash, or protrusions. Inter-lead flash and protrusions shall not exceed .25 mm (.010 in) per side.