

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- Surface-mounted package
- Advanced trench cell design

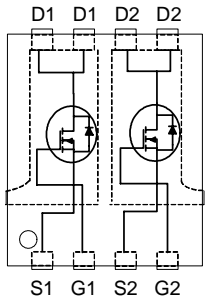
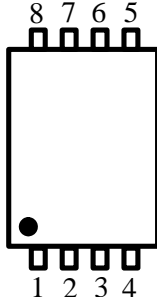
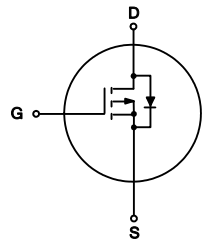
1.2 Applications

- LCD TV appliances
- High power inverter system
- LCDM appliances

1.3 Quick reference

- $BV \cong 60\text{ V}$
- $R_{DS(ON)} \leq 13\text{m}\Omega @ V_{GS} = 10\text{ V}$
- $P_{tot} \leq 35\text{ W}$
- $R_{DS(ON)} \leq 26\text{m}\Omega @ V_{GS} = 4.5\text{ V}$
- $I_D \leq 50\text{ A}$

2. Pin Description

| Pin | Description | Simplified Outline | Symbol |
|-----|---|--|---|
| |  <p style="text-align: center;">Top View PDFN5x6-8L</p> |  <p style="text-align: center;">Top View PDFN5x6-8L</p> |  |

3. Limiting Values

| Symbol | Parameter | Conditions | Min | Max | Unit |
|----------------------|---|---|-----|----------|--------------------|
| V_{DS} | Drain-Source Voltage | $T_C = 25\text{ }^\circ\text{C}$ | - | 60 | V |
| V_{GS} | Gate-Source Voltage | $T_C = 25\text{ }^\circ\text{C}$ | - | ± 20 | V |
| I_D | Drain Current (DC) | $T_C = 25\text{ }^\circ\text{C}, V_{GS} = 10\text{ V}$ | - | 50 | A |
| | | $T_C = 100\text{ }^\circ\text{C}, V_{GS} = 10\text{ V}$ | - | 25 | A |
| $I_{DM}^{*}^{***}$ | Drain Current (Pulsed) | $T_C = 25\text{ }^\circ\text{C}, V_{GS} = 10\text{ V}$ | - | 120 | A |
| P_{tot} | Drain power dissipation | $T_C = 25\text{ }^\circ\text{C}$ | - | 35 | W |
| T_{stg} | Storage Temperature | | -55 | 150 | $^\circ\text{C}$ |
| T_J | Junction Temperature | | - | 150 | $^\circ\text{C}$ |
| I_S | Continuous-Source Current | $T_C = 25\text{ }^\circ\text{C}$ | - | 24 | A |
| $R_{\theta JA}^{**}$ | Thermal Resistance- Junction to Ambient | | - | 50 | $^\circ\text{C/W}$ |
| $R_{\theta JC}^{**}$ | Thermal Resistance- Junction to Case | | - | 3.5 | |

Notes :

* Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

** Mounted on Large Heat Sink

*** limited by bonding wire

4. Marking Information

| Product Name | Marking |
|--------------|--|
| KJ12D06GM | <div style="border: 1px solid black; padding: 2px; display: inline-block;"> 12D06M YWWXXX </div> YWW : Date Code |

5. Ordering Code

| Product Name | Package | Reel Size | Tape width | Quantity | Note |
|--------------|---------|-----------|------------|----------|------|
| KJ12D06GM | DFN5*6 | | | 4000 | |

Note: KUAIJIXIN defines " Green " as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)

6. Electrical Characteristics ($T_A=25^\circ$ Unless Otherwise Noted)

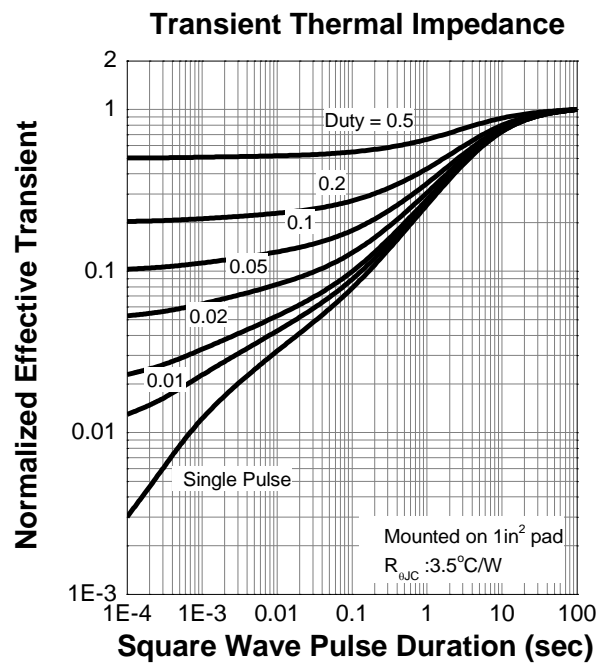
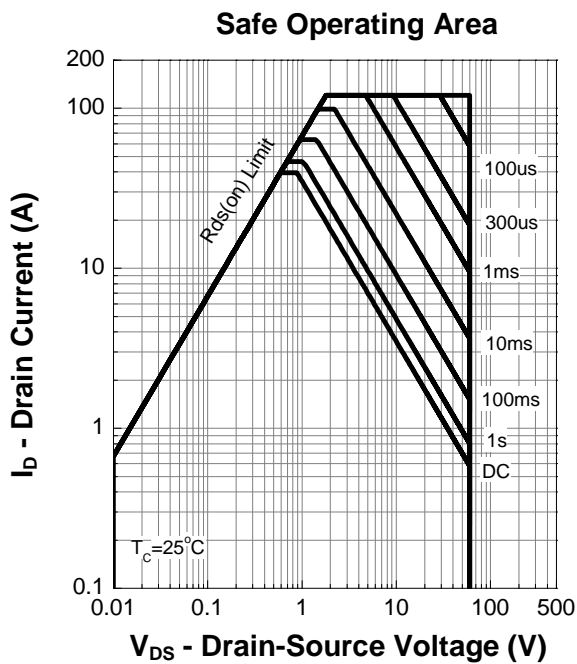
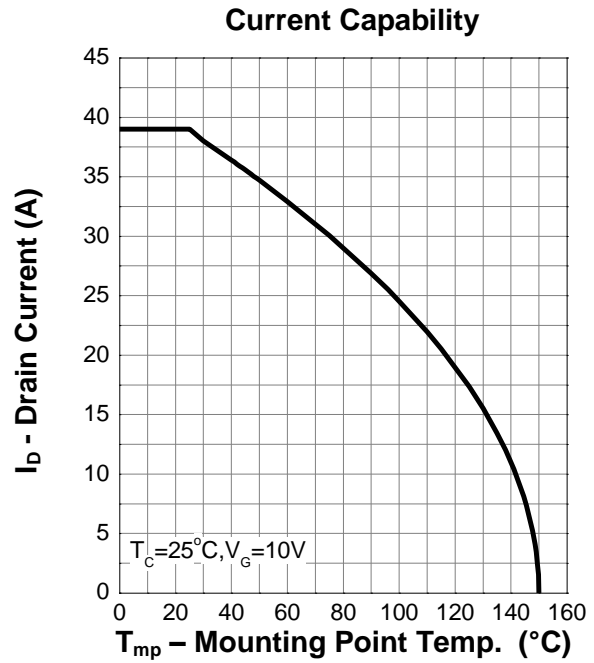
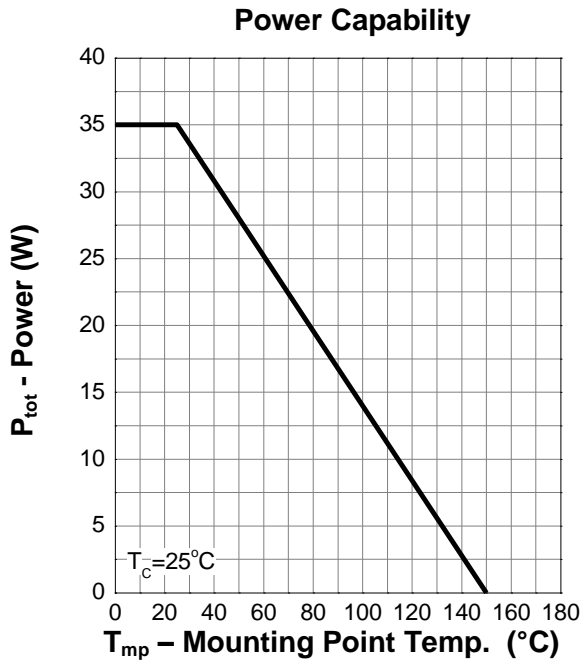
| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|--|--------------------------------|---|-----|-----|-----------|---------------|
| Static Characteristics | | | | | | |
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{GS} = 0\text{ V}, I_{DS} = 250\ \mu\text{A}$ | 60 | - | - | V |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{DS} = V_{GS}, I_{DS} = 250\ \mu\text{A}$ | 1.5 | - | 2.5 | V |
| I_{DSS} | Drain Leakage Current | $V_{DS} = 48\text{ V}, V_{GS} = 0\text{ V}$ | - | - | 1 | μA |
| I_{GSS} | Gate Leakage Current | $V_{GS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$ | - | - | ± 100 | nA |
| $R_{DS(on)}^a$ | On-State Resistance | $V_{GS} = 10\text{ V}, I_{DS} = 12\text{ A}$ | - | 11 | 13 | m Ω |
| | | $V_{GS} = 4.5\text{ V}, I_{DS} = 8\text{ A}$ | - | 20 | 26 | |
| Diode Characteristics | | | | | | |
| V_{SD}^a | Diode Forward Voltage | $I_{SD} = 12\text{ A}, V_{GS} = 0\text{ V}$ | - | - | 1.3 | V |
| t_{rr} | Reverse Recovery Time | $I_{DS} = 12\text{ A}, V_{GS} = 0\text{ V}$ $di_{SD}/dt = 100\text{ A}/\mu\text{s}$ | - | 31 | - | nS |
| Q_{rr} | Reverse Recovery Charge | | - | 11 | - | μC |
| Dynamic Characteristics^b | | | | | | |
| C_{iss} | Input Capacitance | $V_{GS} = 0\text{ V}, V_{DS} = 30\text{ V}$ Frequency = 1 MHz | - | 757 | - | pF |
| C_{oss} | Output Capacitance | | - | 340 | - | |
| C_{rss} | Reverse Transfer Capacitance | | - | 28 | - | |
| $t_d(on)$ | Turn-on Delay Time | $V_{DS} = 30\text{ V}, V_{GEN} = 10\text{ V},$ $R_G = 4.5\ \Omega, R_L = 2.5\ \Omega,$ $I_{DS} = 12\text{ A}$ | - | 6.2 | - | nS |
| t_r | Turn-on Rise Time | | - | 23 | - | |
| $t_d(off)$ | Turn-off Delay Time | | - | 13 | - | |
| t_f | Turn-off Fall Time | | - | 17 | - | |
| Gate Charge Characteristics^b | | | | | | |
| Q_g | Total Gate Charge | $V_{DS} = 30\text{ V}, V_{GS} = 10\text{ V},$ $I_{DS} = 12\text{ A}$ | - | 15 | - | nC |
| Q_{gs} | Gate-Source Charge | | - | 3.6 | - | |
| Q_{gd} | Gate-Drain Charge | | - | 3.6 | - | |

Notes :

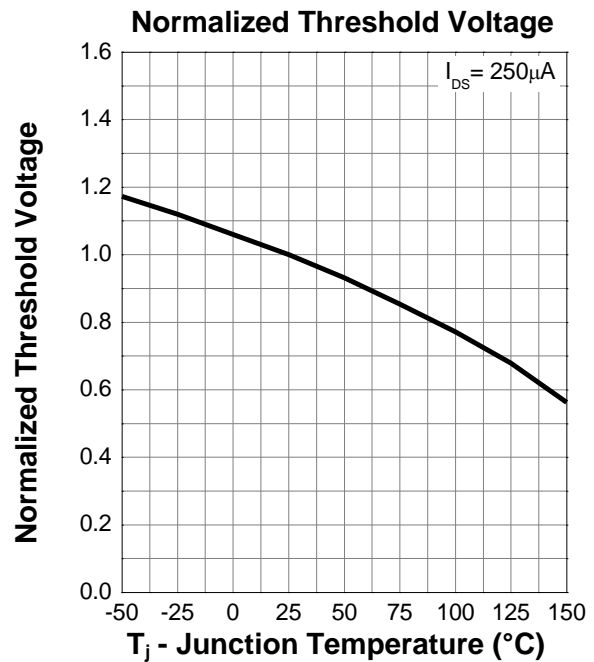
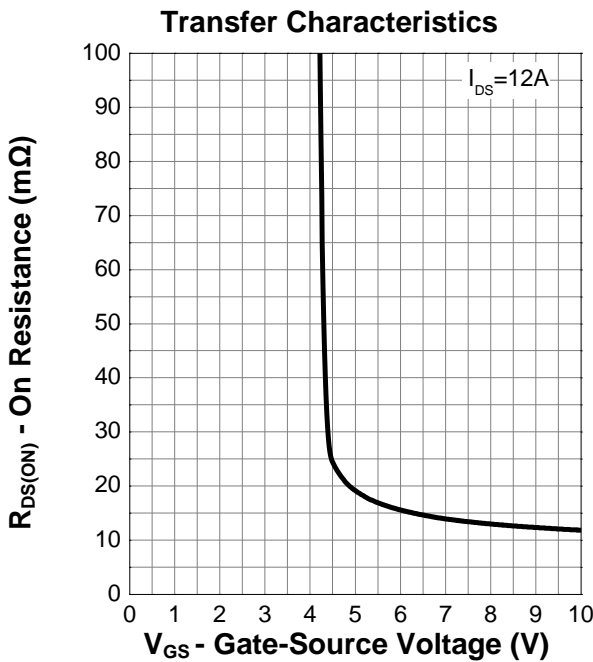
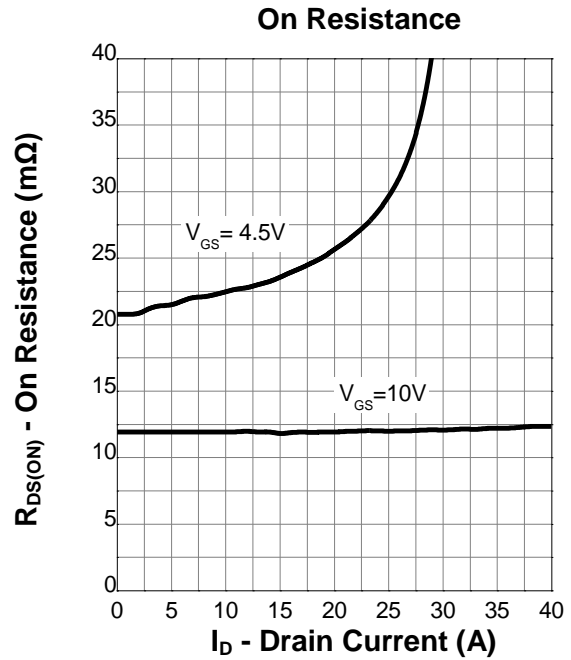
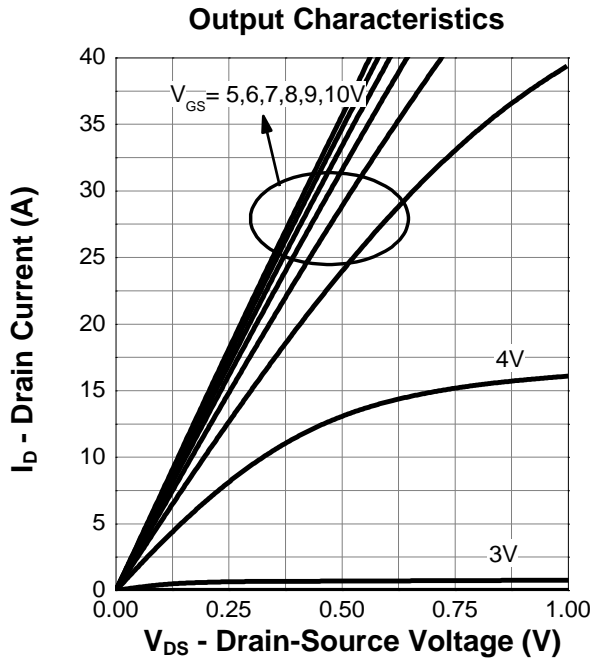
a : Pulse test ; pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$

b : Guaranteed by design, not subject to production testing

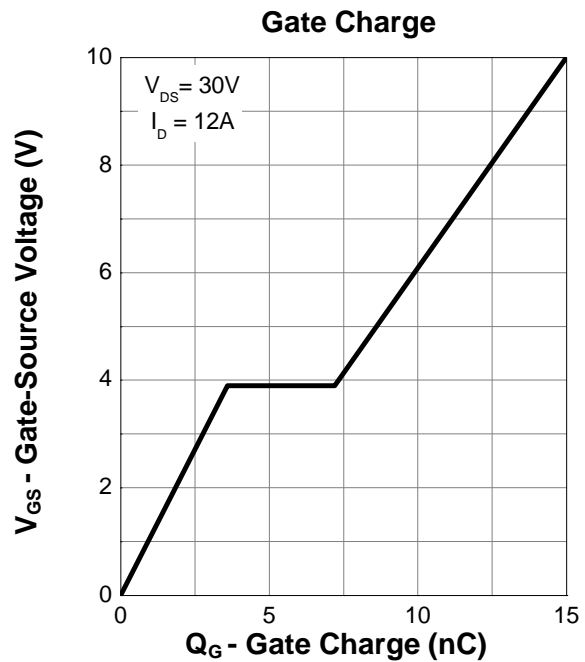
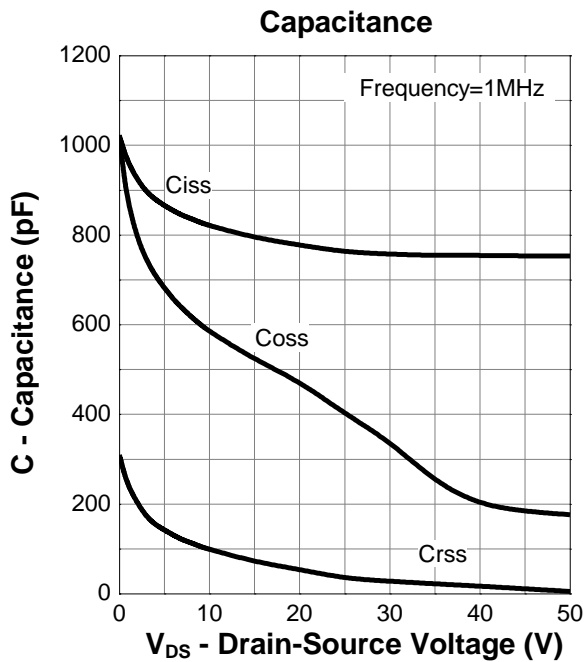
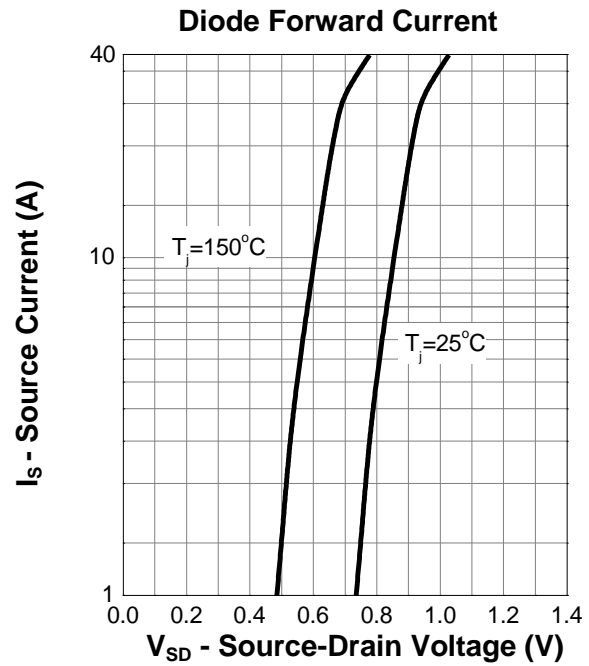
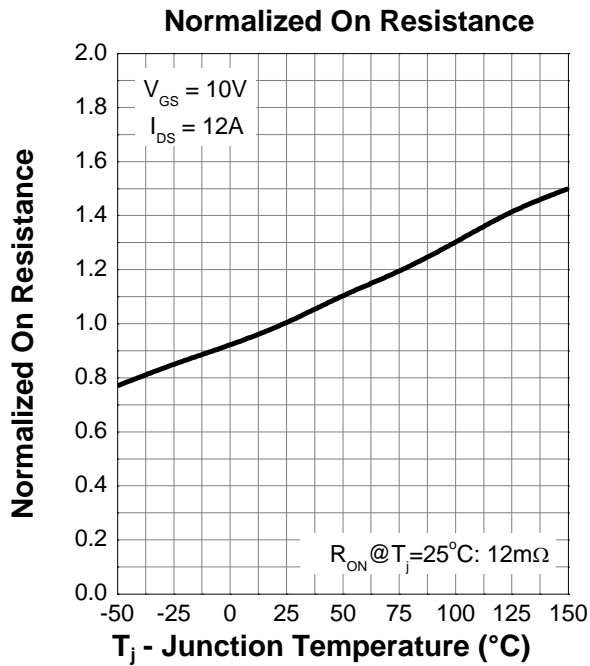
7. Typical Characteristics



7. Typical Characteristics (cont.)



7. Typical Characteristics (cont.)



8.Package Dimensions

DFN5*6

