

P-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- Advanced trench cell design
- Low Thermal Resistance

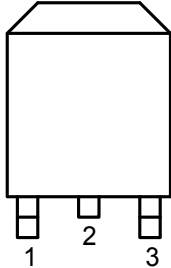
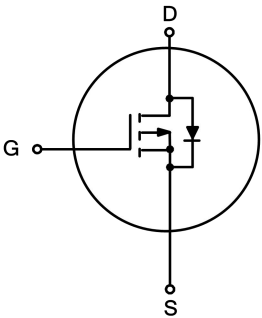
1.2 Applications

- Motor drivers
- DC - DC Converter

1.3 Quick reference

- $BV \geq -40\text{ V}$
- $R_{DS(ON)} \leq 38\text{ m}\Omega @ V_{GS} = -10\text{ V}$
- $P_{tot} \leq 35\text{ W}$
- $R_{DS(ON)} \leq 55\text{ m}\Omega @ V_{GS} = -4.5\text{ V}$
- $I_D \leq -30\text{ A}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Gate(G)	 Top View TO-252-3L	
2	Drain(D)		
3	Source(S)		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_C = 25\text{ }^\circ\text{C}$	-40	-	V
V_{GS}	Gate-Source Voltage	$T_C = 25\text{ }^\circ\text{C}$	-	± 20	V
I_D	Drain Current	$T_C = 25\text{ }^\circ\text{C}, V_{GS} = -10\text{ V}$	-	-30	A
$I_{DM}^{*,**}$	Pulsed Source Current	$T_C = 25\text{ }^\circ\text{C}, V_{GS} = -10\text{ V}$	-	-48	A
P_{tot}^*	Total Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$	-	35	W
T_{stg}	Storage Temperature		-55	150	$^\circ\text{C}$
T_J	Junction Temperature		-	150	$^\circ\text{C}$
I_S	Diode Forward Current	$T_C = 25\text{ }^\circ\text{C}$	-	-30	A
$R_{\theta JC}^*$	Thermal Resistance- Junction to Ambient		-	3.5	$^\circ\text{C} / \text{W}$

Notes :

- * Surface Mounted on 1 in² pad area, $t \leq 10\text{ sec}$
- ** Pulse width $\leq 10\text{ }\mu\text{s}$, duty cycle $\leq 1\%$
- *** Limited by bonding wire

4. Marking Information

Product Name	Marking
KJ30P04K	<div style="display: inline-block; border: 1px solid black; padding: 2px;"> 30P04 YWWXXX </div> YWW: Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ30P04K	T0252			2500	

Note: KUAJIEXIN defines " Green " as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)

6. Electrical Characteristics ($T_C = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

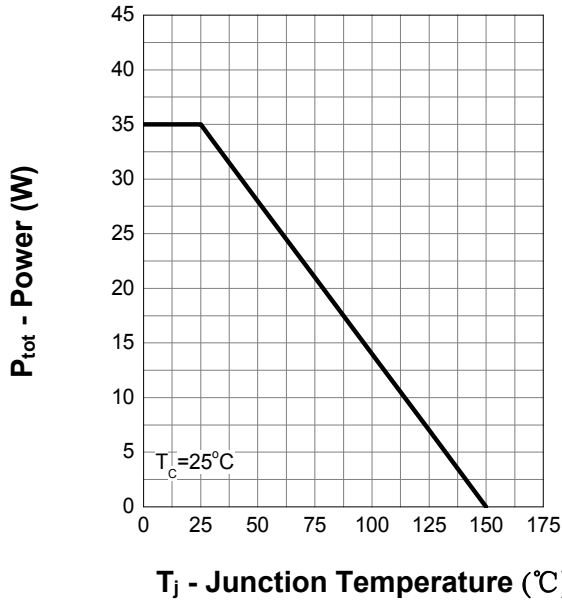
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	-40	-	-	V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{DS} = -250\text{ }\mu\text{A}$	-1.5	-	-2.5	V
I_{DSS}	Zero Gate Voltage Source Current	$V_{DS} = -32\text{ V}, V_{GS} = 0\text{ V}$	-	-	-1	μA
		$T_J = 85\text{ }^\circ\text{C}$	-	-	-30	μA
I_{GSS}	Gate Leakage Current	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$	-	-	± 100	nA
$R_{DS(ON)}^a$	Drain-Source On-State Resistance	$V_{GS} = -10\text{ V}, I_D = -10\text{ A}$	-	33	38	m Ω
		$V_{GS} = -4.5\text{ V}, I_D = -5\text{ A}$	-	46	55	m Ω
Diode Characteristics						
V_{SD}^a	Diode Forward Voltage	$I_{SD} = -10\text{ A}, V_{GS} = 0\text{ V}$	-	-	-1.3	V
t_{rr}	Reverse Recovery Time	$I_{SD} = -10\text{ A}, dI_{SD}/dt = 100\text{ A}/\mu\text{s}$	-	13.5	-	ns
Q_{rr}	Reverse Recovery Charge		-	2.8	-	nC
Dynamic Characteristics^b						
C_{iss}	Input Capacitance	$V_{GS} = 0\text{ V}, V_{DS} = -20\text{ V}$ Frequency = 1 MHz	-	1169	-	pF
C_{oss}	Output Capacitance		-	79	-	
C_{rss}	Reverse Transfer Capacitance		-	64	-	
$t_d(on)$	Turn-on Delay Time	$V_{DS} = -20\text{ V}, V_{GEN} = -10\text{ V},$ $R_G = 4.5\text{ }\Omega, R_L = 2\text{ }\Omega,$ $I_D = -10\text{ A}$	-	13	-	ns
t_r	Turn-on Rise Time		-	43	-	
$t_d(off)$	Turn-off Delay Time		-	135	-	
t_f	Turn-off Fall Time		-	84	-	
Gate Charge Characteristics^b						
Q_g	Total Gate Charge	$V_{GS} = -10\text{ V}, V_{DS} = -20\text{ V},$ $I_{DS} = -10\text{ A}$	-	20	-	nC
Q_{gs}	Gate-Source Charge		-	5.5	-	
Q_{gd}	Gate-Drain Charge		-	2.5	-	

Notes :

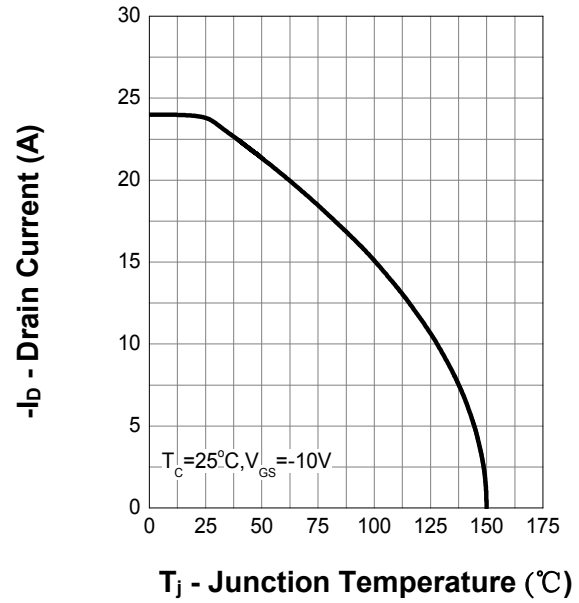
- a : Pulse test ; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
 b : Guaranteed by design, not subject to production testing

7. Typical Characteristics

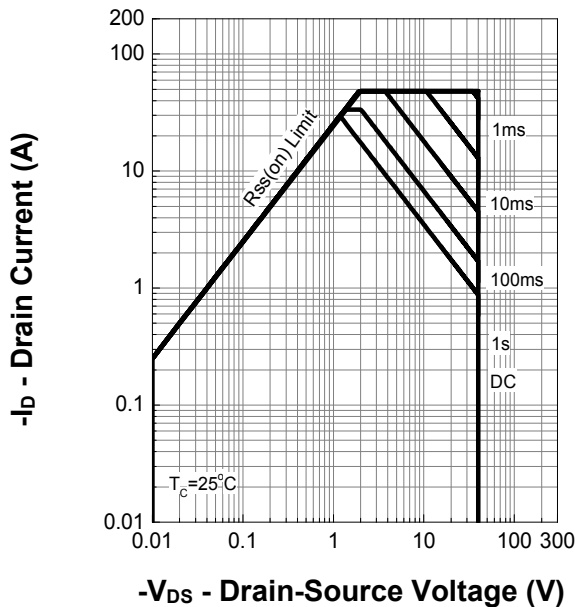
Power Capability



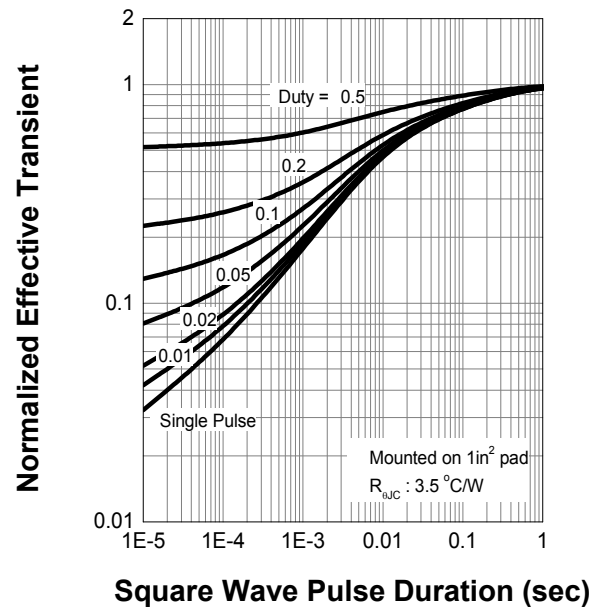
Current Capability



Safe Operation Area

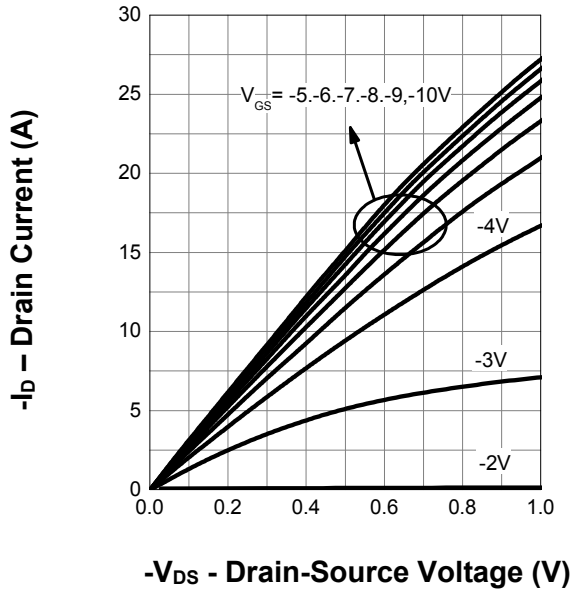


Thermal Transient Impedance

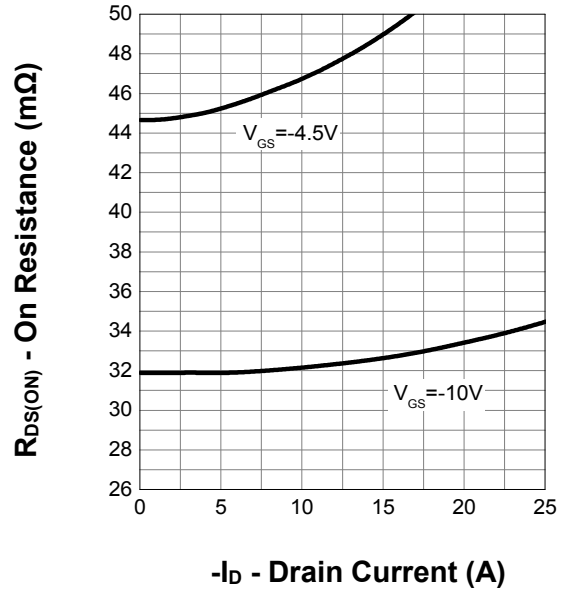


7. Typical Characteristics (cont.)

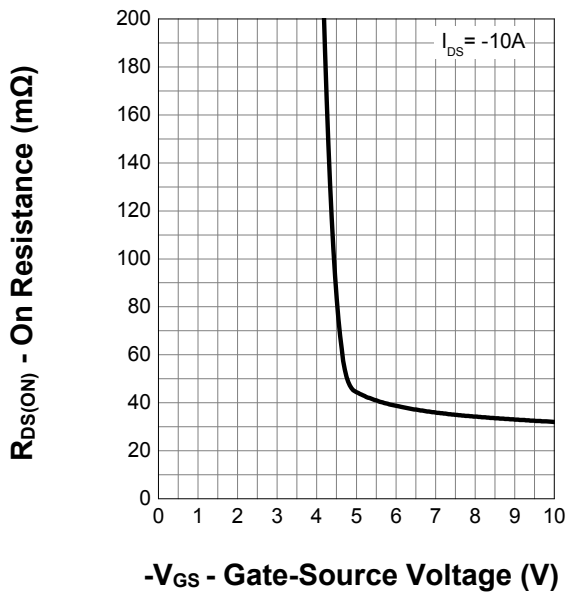
Output Characteristics



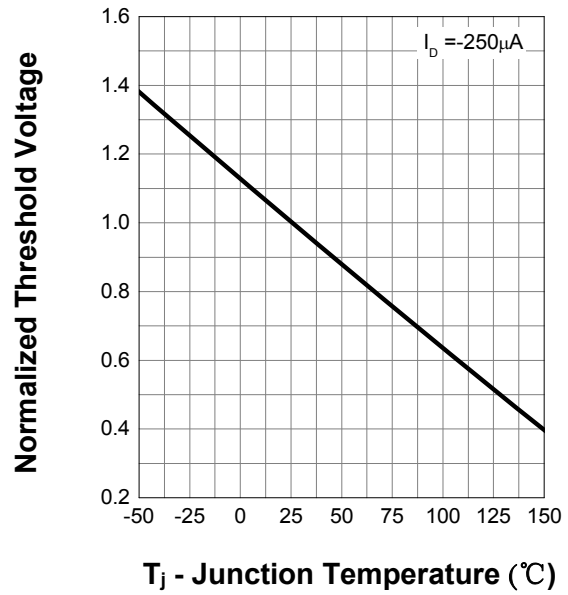
Drain-Source On Resistance



Transfer Characteristics

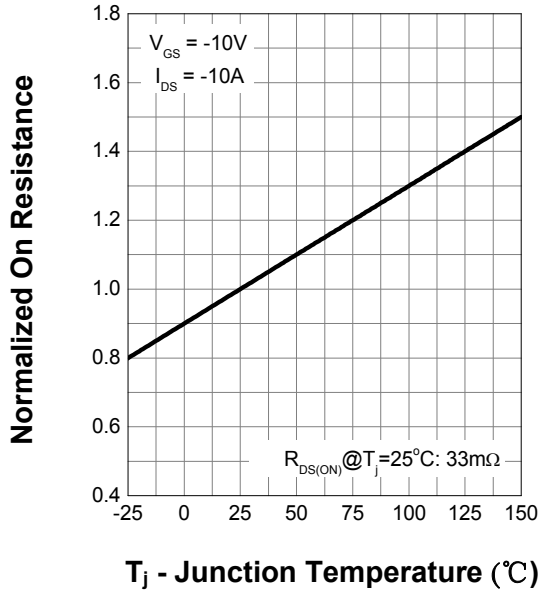


Gate Threshold Voltage

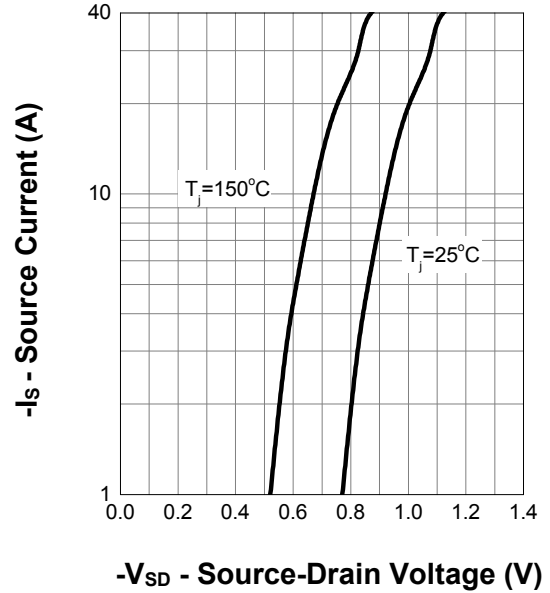


7. Typical Characteristics (cont.)

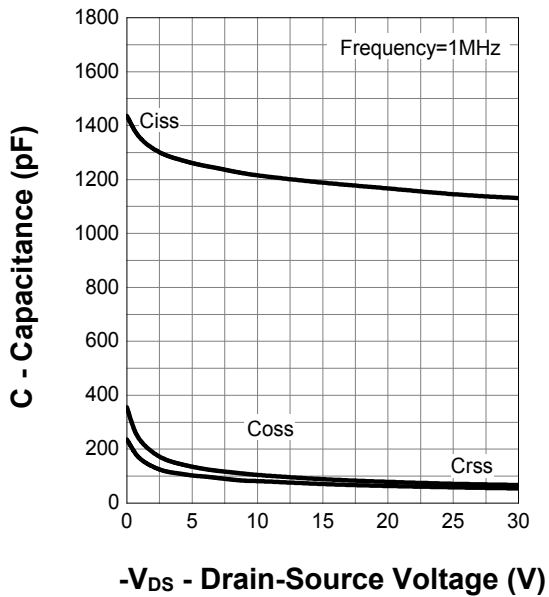
Drain-Source On Resistance



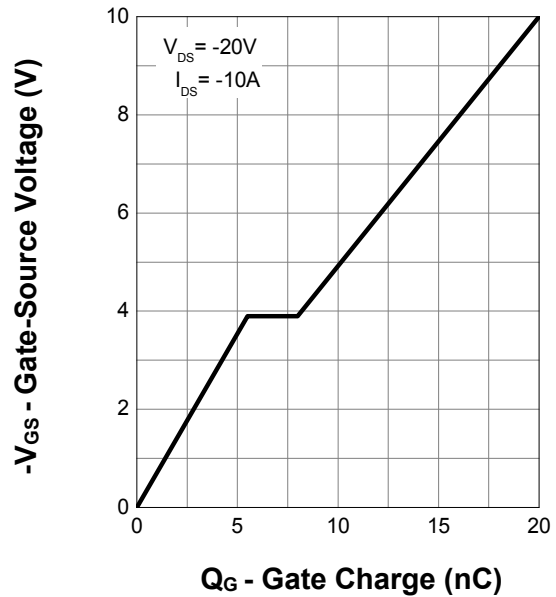
Body Diode Characteristics



Capacitance

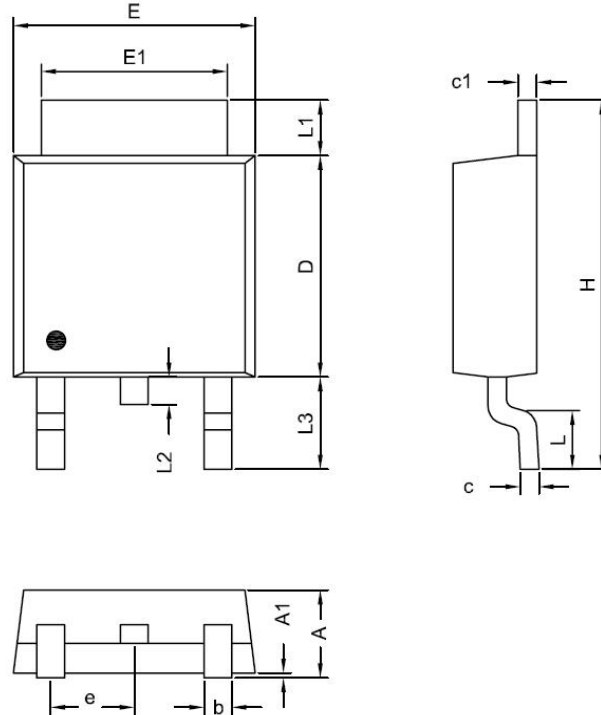


Gate Charge



8.Package Dimensions

TO252-3L



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	2.19	2.38
A1	0.02	0.13
D	5.30	6.40
E	6.35	6.80
E1	5.20	5.50
c	0.40	0.60
c1	0.40	0.60
b	0.55	0.85
e	2.30 BCS	
L	1.00	1.80
L1	0.70	1.80
L2	0.70 BCS	
L3	2.40	2.80
H	9.20	10.40