

P-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- Advanced trench cell design
- Low Thermal Resistance

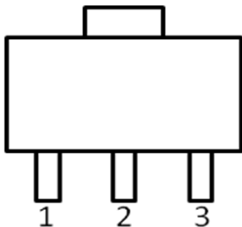
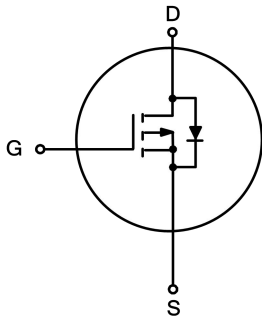
1.2 Applications

- Motor drivers
- DC - DC Converter

1.3 Quick reference

- $BV \geq -40\text{ V}$
- $P_{tot} \leq 25\text{ W}$
- $I_D \leq -10\text{ A}$
- $R_{DS(ON)} \leq 38\text{ m}\Omega @ V_{GS} = -10\text{ V}$
- $R_{DS(ON)} \leq 55\text{ m}\Omega @ V_{GS} = -4.5\text{ V}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1	Gate(G)	 <p>Top View SOT89</p>	
2	Drain(D)		
3	Source(S)		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	Drain-Source Voltage	$T_C = 25\text{ }^\circ\text{C}$	-40	-	V
V_{GS}	Gate-Source Voltage	$T_C = 25\text{ }^\circ\text{C}$	-	± 20	V
I_D	Drain Current	$T_C = 25\text{ }^\circ\text{C}, V_{GS} = -10\text{ V}$	-	-10	A
$I_{DM}^{*,**}$	Pulsed Source Current	$T_C = 25\text{ }^\circ\text{C}, V_{GS} = -10\text{ V}$	-	-25	A
P_{tot}^*	Total Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$	-	35	W
T_{stg}	Storage Temperature		-55	150	$^\circ\text{C}$
T_J	Junction Temperature		-	150	$^\circ\text{C}$
I_S	Diode Forward Current	$T_C = 25\text{ }^\circ\text{C}$	-	-10	A
$R_{\theta JC}^*$	Thermal Resistance- Junction to Ambient		-	3.5	$^\circ\text{C} / \text{W}$

Notes :

* Surface Mounted on 1 in² pad area, $t \leq 10\text{ sec}$

** Pulse width $\leq 10\text{ }\mu\text{s}$, duty cycle $\leq 1\%$

*** Limited by bonding wire

4. Marking Information

Product Name	Marking
KJ10P04K	<div style="display: inline-block; border: 1px solid black; padding: 2px;"> 10P04 YWWXXX </div> YWW: Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
KJ10P04K	SOT89			1000	

Note: KUAJIEXIN defines " Green " as lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900 ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500 ppm by weight; Follow IEC 61249-2-21 and IPC / JEDEC J-STD-020C)

6. Electrical Characteristics (T_C = 25 °C Unless Otherwise Noted)

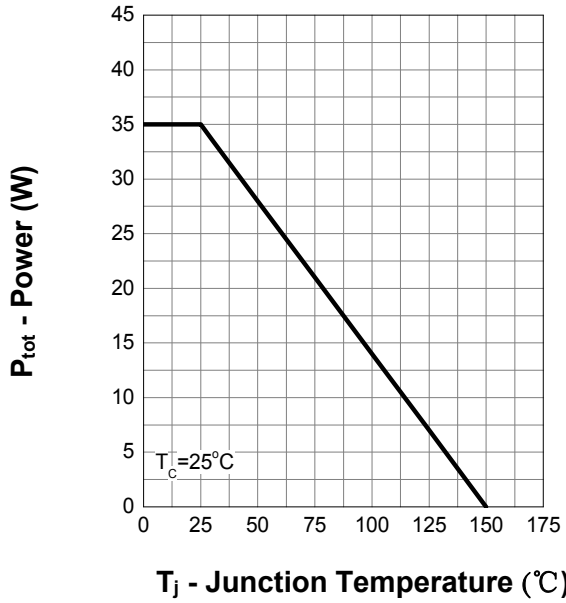
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = -250 μA	-40	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _{DS} = -250 μA	-1.5	-	-2.5	V
I _{DSS}	Zero Gate Voltage Source Current	V _{DS} = -32 V, V _{GS} = 0 V	-	-	-1	μA
		T _J = 85 °C	-	-	-30	μA
I _{GSS}	Gate Leakage Current	V _{GS} = ± 20 V, V _{DS} = 0 V	-	-	± 100	nA
R _{DS(ON)} ^a	Drain-Source On-State Resistance	V _{GS} = -10 V, I _D = -10 A	-	33	38	mΩ
		V _{GS} = -4.5 V, I _D = -5 A	-	46	55	mΩ
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} = -10 A, V _{GS} = 0 V	-	-	-1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} = -10 A, dI _{SD} /dt = 100 A/μs	-	13.5	-	ns
Q _{rr}	Reverse Recovery Charge		-	2.8	-	nC
Dynamic Characteristics^b						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = -20 V Frequency = 1 MHz	-	1169	-	pF
C _{oss}	Output Capacitance		-	79	-	
C _{rss}	Reverse Transfer Capacitance		-	64	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} = -20 V, V _{GEN} = -10 V, R _G = 4.5 Ω, R _L = 2 Ω, I _D = -10 A	-	13	-	ns
t _r	Turn-on Rise Time		-	43	-	
t _{d(off)}	Turn-off Delay Time		-	135	-	
t _f	Turn-off Fall Time		-	84	-	
Gate Charge Characteristics^b						
Q _g	Total Gate Charge	V _{GS} = -10 V, V _{DS} = -20 V, I _{DS} = -10 A	-	20	-	nC
Q _{gs}	Gate-Source Charge		-	5.5	-	
Q _{gd}	Gate-Drain Charge		-	2.5	-	

Notes :

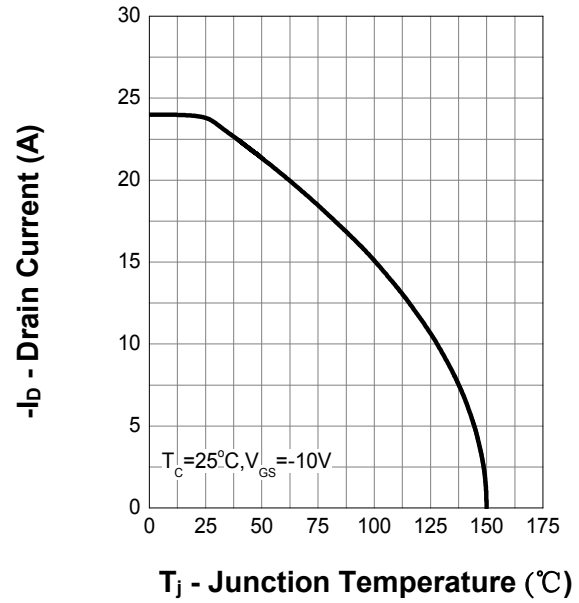
- a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2 %
- b : Guaranteed by design, not subject to production testing

7. Typical Characteristics

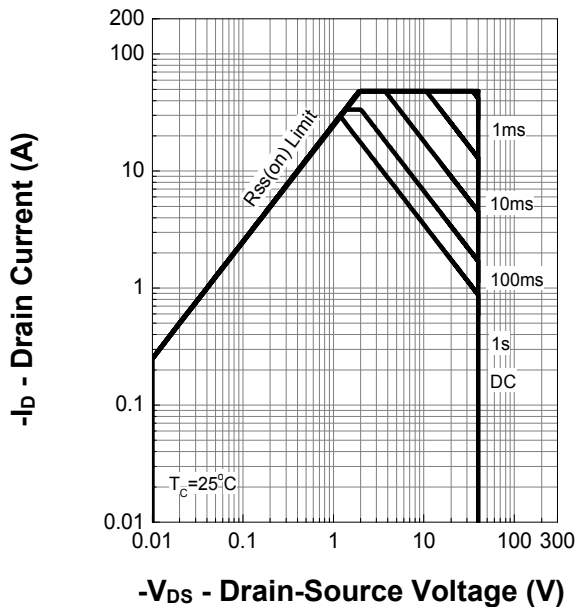
Power Capability



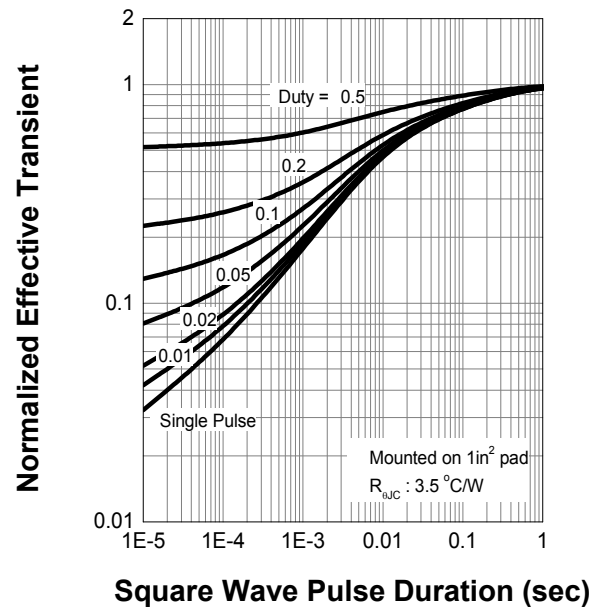
Current Capability



Safe Operation Area

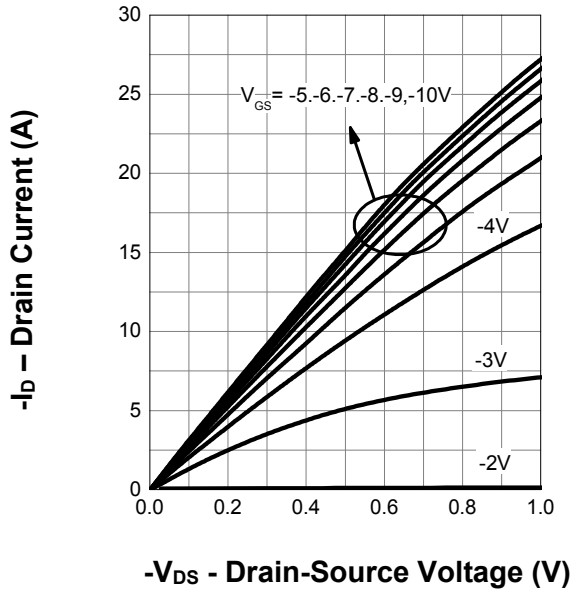


Thermal Transient Impedance

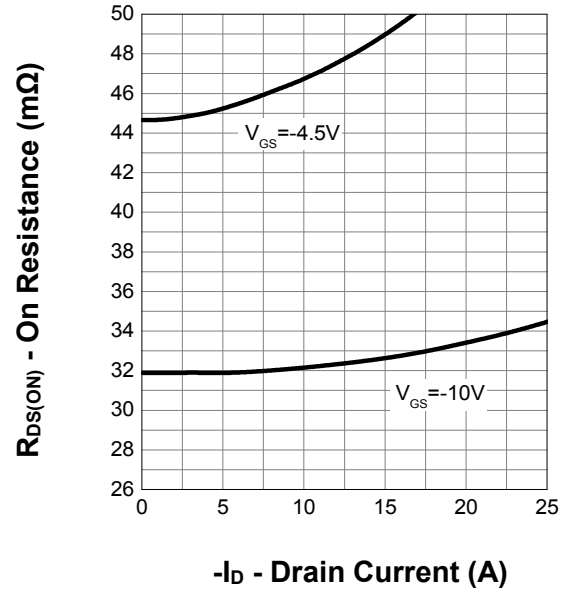


7. Typical Characteristics (cont.)

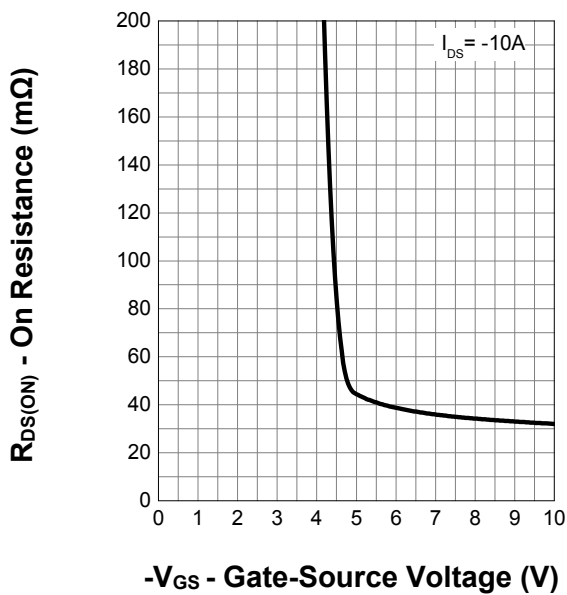
Output Characteristics



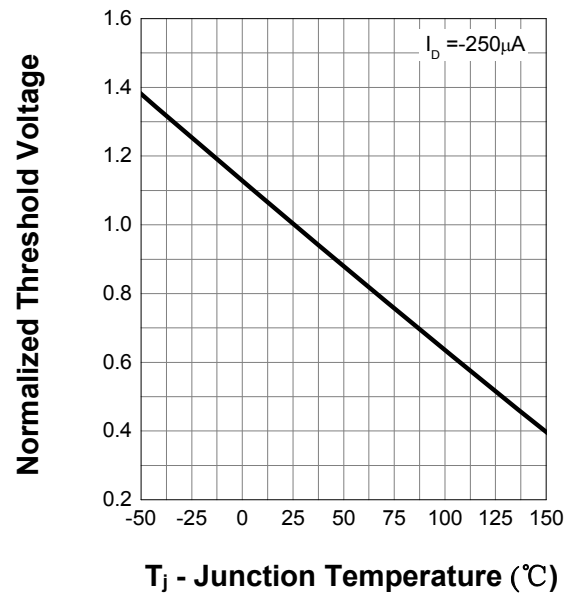
Drain-Source On Resistance



Transfer Characteristics

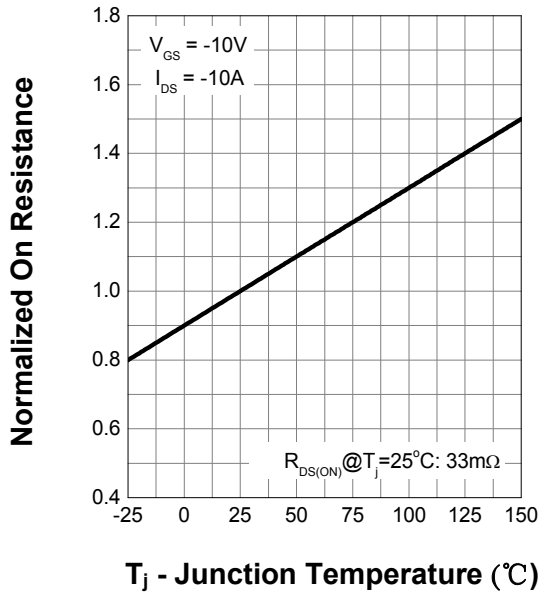


Gate Threshold Voltage

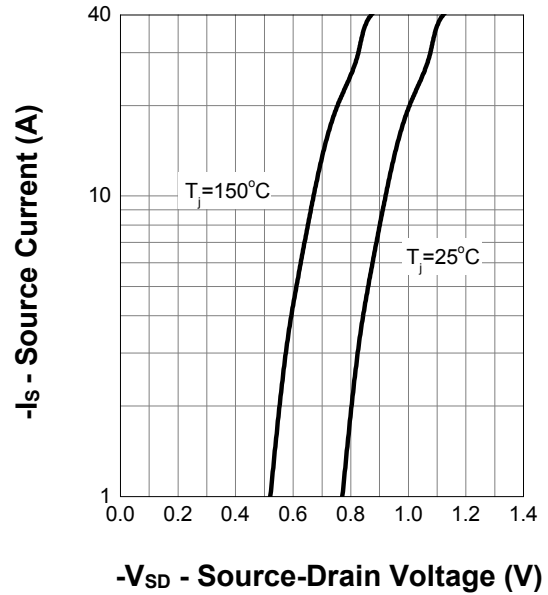


7. Typical Characteristics (cont.)

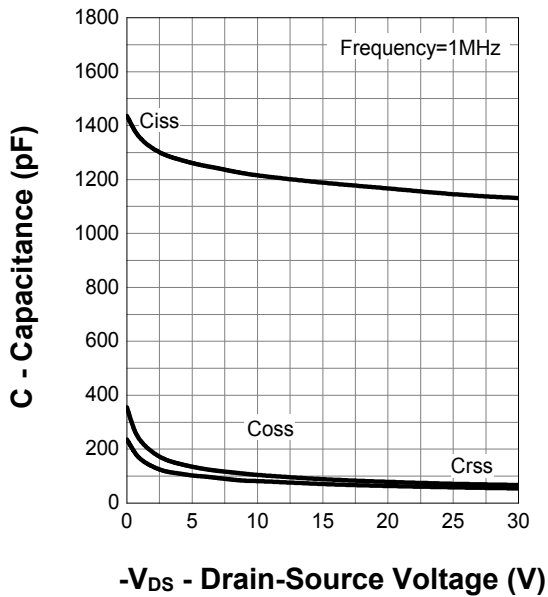
Drain-Source On Resistance



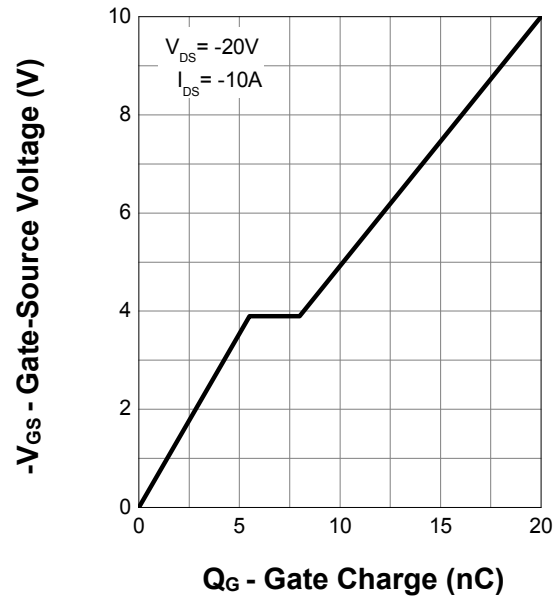
Body Diode Characteristics



Capacitance

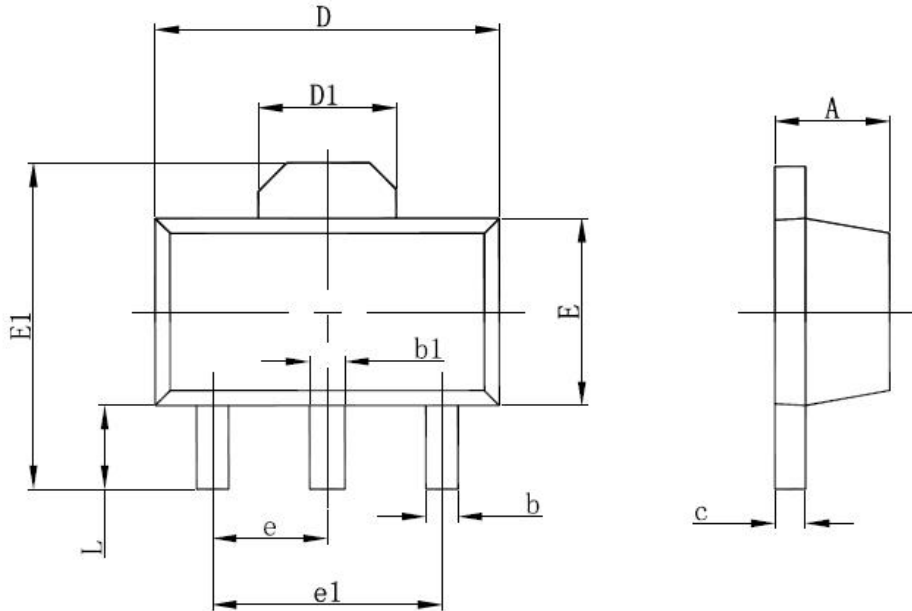


Gate Charge



8.Package Dimensions

SOT89-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF.		0.061 REF.	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP.		0.060 TYP.	
e1	3.000 TYP.		0.118 TYP.	
L	0.900	1.200	0.035	0.047